

ROD TestStand User Guide

<http://www-hep.fzu.cz/~tomasekl/RODTestStandUserGuide.doc>

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1. Introduction

<http://www-hep.fzu.cz/~tomasekl/RODTestStandOverview.ppt>

1.1 System requirements

Hardware: PC with NI PCI-MXI-2 interface card (and NIVXI-2 driver installed), VME crate with NI VME-MXI-2 card, Atlas ROD cards;

Operating system: Windows 9X, NT, 2000, XP;

Developer software: National Instruments LabWindows CVI/5.5 (Measurement Studio).

1.2 Installation

Install the whole RodTS directory on the C drive of your computer, if possible (i.e. C:\RodTS). Include A32 VME address range occupied by RODs (i.e. 0x05000000 – 0x15FFFFFF for slots 5 – 21) into your NI-VXI database using T&M Explorer.

Although the TestStand is designed to be installed virtually anywhere on your computer, on any drive and under any subdirectory, the command and primitive list files supplied with the TestStand contain the absolute paths to all input data files (usually C:\RodTS\Data).

Otherwise, the lists must be converted to point to your actual directory. (This “feature” can be changed in the future so that all file paths will be relative inside the RodTS directory. It’d also mean all input data files being restricted to RodTS subdirectories.)

1.3 Starting the TestStand

Start executable **RodTS\Rcc\rcc_SCT.exe** or **rcc_PIXEL.exe** (SCT or Pixel ROD version).

A popup window “**Run RESMAN and InitVXIlibrary()**?” will appear. Confirm if want to communicate with the RODs and have all necessary hardware ready → VXI Resource Manager will be executed and VXI library initialized. Choose “No” if you only want to edit primitive and command lists on a computer without VME access (you can also run Resman and initialize VXI libraries anytime later, clicking on RESMAN button on the main TestStand panel).

Next dialog box “**Delete ErrorFile and all RodData files?**” gives you an option to delete all status files from the previous TestStand run - the *ErrorFile* (*RodTS\Data>ErrorFile.txt*) and all Rod output data binary files, text buffer files, command list status files etc. (in *RodTS\Data\Rod\Slot#* directories). If these “old” files aren’t deleted, all new error and status text messages (ErrorFile, TextBuffer files, CommandList status) will be simply appended at the end.



Then the main panel is opened:

2. Main Panel

The screenshot shows the 'RCC MAIN WINDOW' with a menu bar (LoadRod!, Params, ErrorFile!, ListEditor, MemoryAccess, ExitProgram!) and a title bar. The main area is titled 'CRATE STATUS/CONTROL' with version 'v5/21/03'. It contains several control panels: 'ShowErrorWindow' (checked), 'RESMAN' (green), 'SYS RESET' (red), 'RESET RODs' (yellow), 'VME byte order' (INTEL), 'Config' (SCT), 'Rod Count' (0), 'ActiveSlot#' (0), 'DefaultSlot#' (20), 'CommLoop' (SUSPENDED), 'RODs' (DISABLED), 'Status Refresh' (TestStand, FPGA Monitor, DSP Monitor), 'Thread Priority' (UIR, CommLoop, CmdListExecution), 'Timeout[sec]' (rodInit, textBuff, dspAckClear, dspAckSet), 'CmdListBusy RODslot#' (5-21), 'Slot#' (ALL), 'COMMAND LIST' (Repetitions, If Error, file path, ADD, REMOVE, EDIT, SEND CMD LIST), and 'PRIMITIVE LIST' (Repetitions, EXEC BUILD, SaveLists, ChecksumMaster, ChecksumSlave, file path, ADD, REMOVE, EDIT, SEND PRIM LIST).

This is the TestStand global control/status window. It gives you the access to all TestStand functionality and the other function panels.

Main Panel menu:

- **LoadRod** (*see chapter 4.*)
- **Params**
 - Save list rings (saves current set of default primitive and command lists inside  buttons to a cfg text file)
 - Load list rings (load set of default lists from a cfg file to  buttons)
- **Error File** – opens the Error File (*RodTS\Data>ErrorFile\CommonErrorFile.txt*), which collects all error messages encountered during TestStand operation (error type and the location in the source code by file name and line number). Provide this file if having trouble with TestStand functionality.
- **List Editor**
 - Primitive List (*chapter 5.*)
 - Command List (*chapter 6.*)
- **Memory Access**
 - TIM (opens the TIM panel with basic R/W access to Tim registers)
 - VME (*chapter 3.3*)
 - Master DSP (*chapter 3.4*)
 - Slave DSP (*chapter 3.5*)
 - FPGA Status/Control Regs. (*chapter 3.2*)
 - Flash (*chapter 3.1*)
 - View Bin File - opens the external binary viewer Witched installed in *RodTS\Witched* directory (this tool gives you the option to watch binary file in hex, binary or text representation, compare it with another file etc.)
- **Help** – opens the directory *RodTS\TestStandUserGuide* with TestStand documentation (this User Guide, Overview ...)
- **Exit** – program exit (Note: The standard Windows “kill” cross button doesn’t work on Windows 9X, NT and 2000)

X) More Main Panel details later

3. Memory Panels

3.1 FLASH Panel

The screenshot shows the 'FLASH' utility window with a menu bar containing 'MainPanel!', 'SaveCurrentSettings!', 'LoadSettings!', 'ViewBinFile!', and 'Hide!'. A 'Slot#' dropdown is set to '18'. The panel is divided into several sections:

- LOCATION FILE**: File path 'c:\Rodts\Data\Fpga\locationFile_v0.bin'. Fields for Size[B] (x000018), Sectrs (d 1), Start addr (x E00000), and End addr (x E00017). Buttons: Read, Write.
- RRIF FILE**: File path 'c:\RodTS\Data\Fpga\revC\rcf_v24c.bin'. Fields for Size[B] (x078E64), Sectrs (d 121), Start addr (x E01000), and End addr (x E79E63). Buttons: Read, Write.
- FORMATTER FILE**: File path 'c:\RodTS\Data\Fpga\revC\sct_xp6.bin'. Fields for Size[B] (x052330), Sectrs (d 83), Start addr (x E80000), and End addr (x ED232F). Buttons: Read, Write.
- EFB FILE**: File path 'c:\RodTS\Data\Fpga\revC\efb_s13c.bin'. Fields for Size[B] (x052328), Sectrs (d 83), Start addr (x ED3000), and End addr (x F25327). Buttons: Read, Write.
- ROUTER FILE**: File path 'c:\RodTS\Data\Fpga\revC\rtr_s16c.bin'. Fields for Size[B] (x0393D8), Sectrs (d 58), Start addr (x F26000), and End addr (x F5F3D7). Buttons: Read, Write.
- Flash Memory Management**: A section with three rows of buttons and start addresses:
 - Erase FLASH0 (Start addr: x E00000) and Read FLASH0
 - Erase FLASH1 (Start addr: x E80000) and Read FLASH1
 - Erase FLASH2 (Start addr: x F00000) and Read FLASH2
- DSP PROGRAM**: File path 'c:\RodTS\Data\DSPProgram\rodRun\rodRun_FLASH.bin'. Fields for Size[B] (x04DD2C), Sectrs (d 78), Start addr (x01400000), and End addr (x0144DD11). Buttons: Write, Erase FLASH DSP.

This panel is a utility for writing and reading all Flash memories on the Rod. The Flashes 0, 1 and 2 contain FPGA code, the Master DSP code is loaded in the DSP Flash. To write the selected file to Flash, press **WRITE** button (click on the file name box to select another file). Read Flash content using **READ** buttons (and enter the file name for storage). You can also erase flashes by **Erase FLASHx**. If you want to read Masted DSP Flash, do it from the *Master DSP Memory Panel* (chapter 3.4).

To save and retrieve different flash configurations, use **SaveCurrentSettings** and **LoadSettings** from the panel menu bar.

Note: Writing and reading flashes can be also done via *CommandList* (chapter 6.).

3.2 FPGA Program/Reset Panel

FPGA STATUS/COMMAND REGS

MainPanel! Hide!

ROD ID: Code Version: Slot#: **18** ROD S/N: ROD Revision: InMem Size:

STATUS

FpgaCnfg	FpgaReset	VmeDspReset	Fpgalnit	Flash	FlpgaHalt
S0 <input type="text" value="1F"/>	S1 <input type="text" value="1F"/>	S2 <input type="text" value="3E"/>	S3 <input type="text" value="1F"/>	S4 <input type="text" value="00"/>	S5 <input type="text" value="00"/>
7	7	7	7	7	7
Cnfg Ovrd 6				6	6
Cnfg En 5				5	5
Router Done 4	Router 4	SDSP3 5	Router Init 4	4	Router Halt 4
EFB Done 3	EFB 3	SDSP2 4	EFB Init 3	3	EFB Halt 3
FormB Done 2	Form B 2	SDSP1 3	FormB Init 2	2	FormB Halt 2
FormA Done 1	Form A 1	SDSP0 2	FormA Init 1	1	FormA Halt 1
RRIF Done 0	RRIF 0	MDSP 1	RRIF Init 0	0	RRIF Halt 0

CONTROL

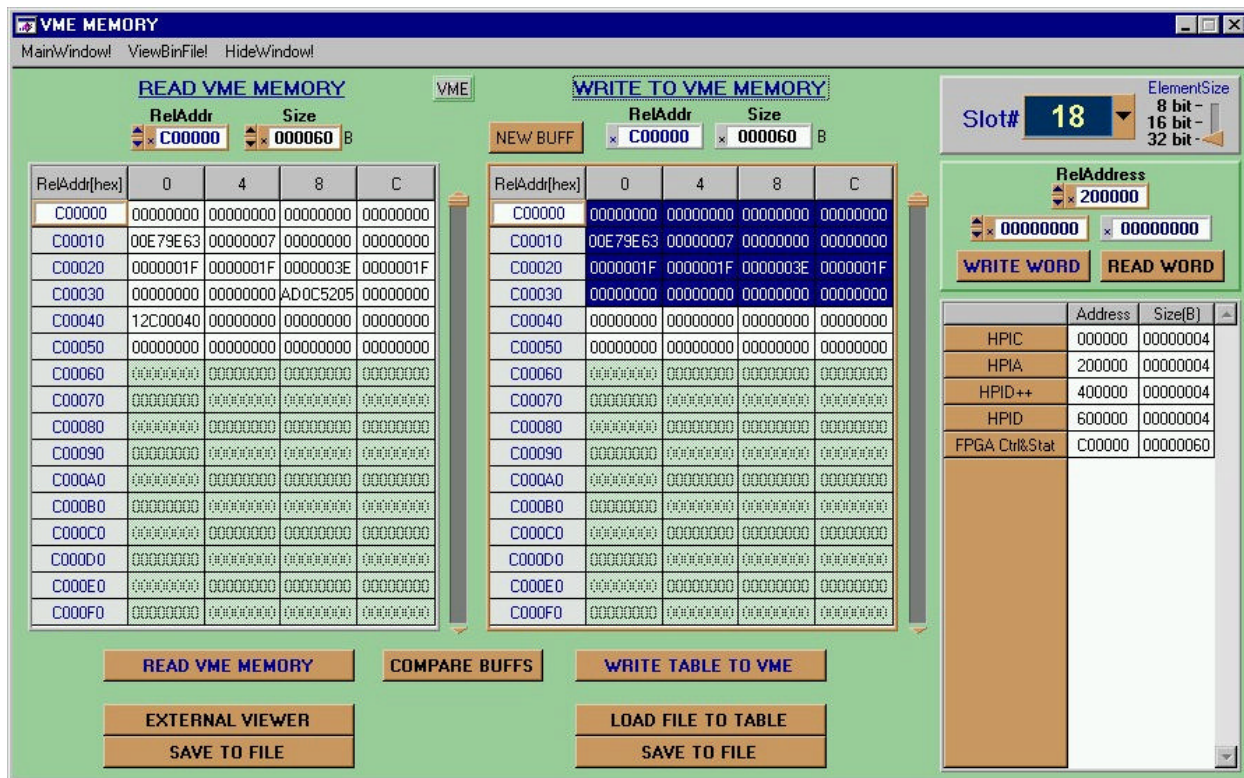
FpgaCnfg	FpgaReset	VmeDspReset	Flash	FlashAddr+WtData
C0 <input type="text" value="00"/>	C1 <input type="text" value="00"/>	C2 <input type="text" value="00"/>	C3 <input type="text" value="00"/>	C4 <input type="text" value="00E79E63"/>
7	7	7	7	
Cnfg Ovrd 6 OK	Hold FPGA 6 OK	Reset board 6 OK	6	
CnfgAll 5 OK	Reset all FPGA 5 OK	Reset SDSP3 5 OK	5	
CnfgRouter 4 OK	Reset Router 4 OK	Reset SDSP2 4 OK	4	
CnfgEFB 3 OK	Reset EFB 3 OK	Reset SDSP1 3 OK	3	
CnfgFormA&B 2 OK	Reset FormB 2 OK	Reset SDSP0 2 OK	2	FlashRdData
CnfgFormA&B 1 OK	Reset FormA 1 OK	Reset MDSP 1 OK	1	S7 <input type="text" value="00"/>
CnfgRRIF 0 OK	Reset RRIF 0 OK	0 OK	0	

READ ALL REGISTERS

This panel provides basic FPGA status information - press **READ ALL REGISTERS** button to refresh it. The control registers bits are used to reset and configure main functional parts of the Rod (pressing OK button sets immediately this bit in the appropriate register on the Rod).

Note: You can optionally use VME Memory Panel (chapter 3.3) or *CommandList* with the R/W VME commands instead (chapter 6.).

3.3 VME Memory Panel



This panel can be used for simple read/write access to any VME memory space on Rod or any VME accessible device in the crate. You can read or write one register using the “word” controls in the upper right corner below slot number or memory block using block VME transfer (left table and controls for readout, right table and controls for writing). The table on the right side of the panel contains the default memory locations – name, address and size for each.

For any type of access first select the slot number in the upper right corner and element size (ROD uses 32bit wide words).

Register Access

Edit the address (i.e. relative lower 24 bits, the upper 8 bits are set by slot number) and press **READ WORD** button to read the data from register (the retrieved value is printed in the control above the button). To write value to register, edit the data field to be written and press **WRITE WORD** button.

If you want to copy any address from the default “address table”, select the address value and copy it to any address control using standard Windows copy (**Ctrl+C**) and paste (**Ctrl+V**) functions.

Block Read

Edit manually the desired starting address and the block size in bytes or use predefined values from the address table. Just double click on the desired field, the address and size values will be

automatically copied to the appropriate fields. Then press the **READ VME MEMORY**, the readout data will be loaded into the table. If the data block is larger than the table size, you can scroll up or down using the slider on the right side (pressing the up or down “arrows” will move just one page). Or edit the address you want to see in the first address box (address just below Addr[hex] label) and press enter. For a larger block of data it’s more convenient to open the data in the external binary viewer Witched – use **EXTERNAL VIEWER** button. The loaded data can be saved with **SAVE TO FILE** button.

Block write

First you have to load the “write table” with data to be written.

Press **NEW BUFF**, the window where you can edit the desired starting address, block length and default value for all words will appear. If you want to select the default address and length from the address table, double click on the desired address field BEFORE clicking on **NEW BUFF**.

Or you can load the table with data from any binary file using **LOAD FILE TO TABLE** button instead.

After the memory buffer was initially loaded, you can edit any data word in the table (double click on the word you want to edit and change the value). You can also use standard copy and paste functions (**CTRL+C**, **CTRL+V**). To copy a block of data from the read table to write table select the data block you want to copy, **Ctrl+C**, select a BLOCK (not only first word!) where you want to paste it, **Ctrl+V**.

To save the data from table to binary file use **SAVE TO FILE** button (i.e. the write table can be used as a simple binary editor*).

Whenever you want to write data from the table to the VME destination press **WRITE TABLE TO VME**.

The **COMPARE BUFFS** button quickly compares the data *word by word* in the read and write table (both buffers must be the same size!).

Optionally you can use Witched viewer for file comparisons.

**Note:* Another binary file editor utility is also attached (*RodTS\DataFileCreator\DataFileCreator.exe*; can be also opened directly from PrimListEditor and CommandListEditor menus).

3.4 Master DSP Memory Panel

The screenshot shows the 'MASTER DSP MEMORY' window with a menu bar (MainWindow!, ViewBinFile!, HideWindow!). It features two main sections: 'READ MDSP MEMORY' and 'WRITE TO MDSP MEMORY'. Both sections have 'Address' and 'Size' input fields (e.g., 02052000, 00040000) and a 'NEW BUFF' button. Below these are two large tables for memory data. The 'READ' table has columns for 'Addr[hex]' and four data columns (0, 4, 8, C). The 'WRITE' table has similar columns. To the right, there's a 'Slot#' dropdown (20) and 'Address' input (01840000) with 'WRITE WORD' and 'READ WORD' buttons. At the bottom, there are buttons for 'READ MASTER MEMORY', 'COMPARE BUFFS', 'WRITE TABLE TO MASTER', 'EXTERNAL VIEWER', 'LOAD FILE TO TABLE', and 'SAVE TO FILE'. A 'repetitions' counter is set to 0. A 'PIO/DMA' section shows 'PIO' selected and 'Time(sec)' as 0.111. A 'BlockAccess' section has 'Yes' selected. A 'Delay(ms)' field is set to 0. On the far right, a table lists various hardware components with their addresses and sizes.

	Address	Size(B)
EFB FPGA	00402000	00000280
EFB Diag Regs	00402200	00000080
Router Trap	00402400	00000100
Router S-Link	00402500	00000010
Controller FPGA	00404400	00000100
FE Occupancy	00404500	00000030
ModeBits LUT	00404600	00000080
EFB DMask LUT	00404700	00000074
BOC Status	00408F00	00000064
BOC	00408000	00000F64
Slave0 HPI	00780000	00000010
Slave1 HPI	007A0000	00000010
Slave2 HPI	007C0000	00000010
Slave3 HPI	007E0000	00000010
RDD Status	80000000	0000000C
VME Command	8000000C	00000008
Expert Cmd	80000010	00000004
Histogram Cmd	80000020	00000008

Access to Master DSP memory. Similar user interface to VME panel, but addressing is within the Master DSP memory space.

Additions:

Time[sec] box - the last block R/W access time to MasterDSP memory (in seconds).

Repetitions - any block transfer can be repeated more times (the number in this box means additional repetitions, so 0 reads or writes block once, 1 twice etc.), the counter update can be disabled (for timing measurements).

PIO/DMA option - selection between programmed I/O and DMA access to VME (DMA access is not reliable at this moment!!).

Block Access option "Yes"- MasterDSP memory space accessed by fast VME block transfers.

Block Access option "No"- the data block from the table is written to MasterDSP memory space "word by word" with additional delay between words (e.g. setting BOC registers).

3.5 Slave DSP Memory Panel

The screenshot shows the 'SLAVE DSP MEMORY' panel with the following components:

- READ SDSP MEMORY:** Address 02038000, Size 00008000. A table of memory addresses (02038000 to 020380F0) with columns for Address, 0, 4, 8, and C.
- WRITE TO SDSP MEMORY:** Address 00000000, Size 00000000. A table of memory addresses (00000000 to 000000F0) with columns for Address, 0, 4, 8, and C.
- Slave# 3 Slot# 20:** Selection controls for the slave and slot.
- WRITE WORD / READ WORD:** Buttons for writing to or reading from the selected address (00000000).
- Peripheral List:** A table listing various peripherals and their addresses/sizes.

	Address	Size(B)
SDSP Status	80000000	0000000C
MDSP Command	8000000C	00000008
Expert Cmd	80000010	00000004
Histogram Cmd	80000020	00000008
Histogram Stat	80000028	00000008
Trap Stat 0	80000014	00000004
Trap Stat 1	80000018	00000004
Error Buffer	02030000	00008000
Info Buffer	02038000	00008000
Diag Buffer	02040000	00008000
Prim Buffer	02052000	00020000
Reply Buffer	02072000	00020000
RouterOutFifo	01400000	00000100
EMIF Control	01800000	00000020
DMA Control	01840000	00000074
IPRAM	00000000	00010000
- Buttons:** READ SLAVE MEMORY, COMPARE BUFFS, WRITE TABLE TO SLAVE, EXTERNAL VIEWER, SAVE TO FILE, LOAD FILE TO TABLE, SAVE TO FILE.
- PIO/DMA:** Radio buttons for PIO and DMA (exper.).
- Time(sec):** A slider control set to 0.015.

Similar to the Master DSP Panel, but the memory space is within the selected Slave DSP memory (you have to select the slave number 0 to 3).

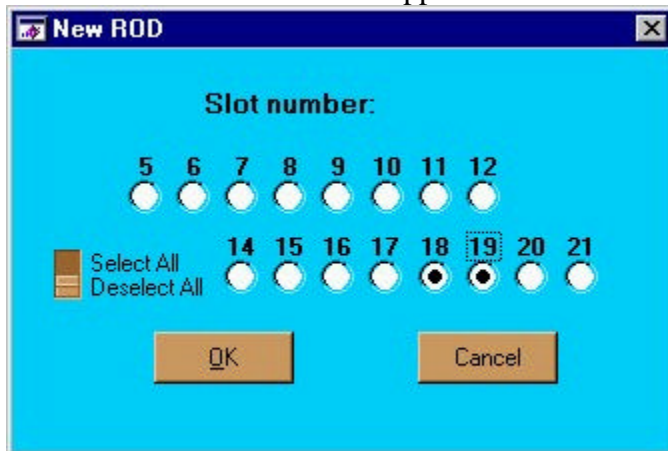
Note: If the slave is running (i.e. the program has been loaded and started) and there is any inter MasterDSP-SlaveDSP communication going on, you can access the slave memory using this panel only **AFTER** the Master-Slave communication has been stopped by setting *DmaRequest* bit in MasterDSP *CommandRegister* (from ROD Panel or using primitive *ConfigSlave*)!

4. ROD[n] Panel

If you want to control any Rod using command and primitive lists and periodically check its' status, the Rod must be loaded into TestStand and started.

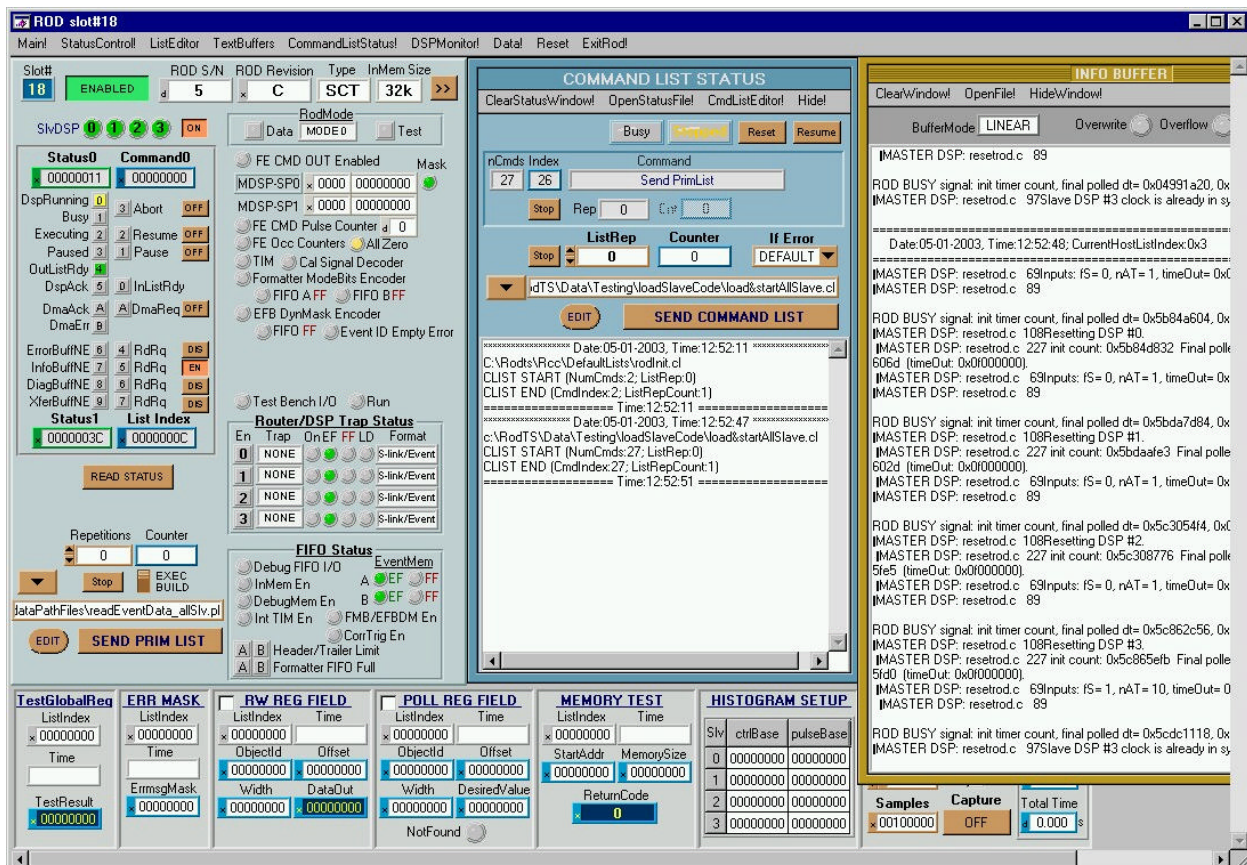
Load the *Rod status/control window* using **LoadRod** menu item in the *Main Panel*.

The slot number selector will appear:



Select all slots you want to use (naturally, the Rod board should be loaded in the corresponding slot). A new Rod can be added to the system anytime, you don't have to load all at once.

For each selected slot the *RODStatus/Control window* is loaded:



XX) More info about Rod Panel later

5. Primitive List Editor

PRIM LIST EDITOR

Main! CommandListEditor! DataFileCreator! RevisionUpdate! HideWindow!

PRIMITIVE

MASTER ▾

RW_FIFO

INPUT DATA

READ WRITE

FifoD&Bank

INPUT_MEM - A ▾

FifoSize[elems]

Input x 8000 48b
 Debug x 1000 48b
 EventAB x 4000 48b
 EventC x 00FF 32b
 Tim x 2000 8b

NumElements

x 00001000

INPUT FILE

c:\RodTS\Data\Testing\fifoTesting\4KRandom1.bin

OUTPUT DATA

SAVE OUTPUT DATA TO FILE? ☐

OUTPUT FILE

Write random data from file to Input FIFO A.

Slot# ALL ▾

Repetitions 0

SEND TO ROD

Ctrl+ ADD BEFORE>>

REPLACE>>

ADD AFTER>>

ViewPrim ↑ ↓ Ctrl+ <<EDIT PRIM

Ctrl+Del DELETE PRIM

LOAD LIST

INSERT LIST

SAVE LIST

DELETE LIST

MASTER LIST

	ListLength	PrimCount	AllPrimCount
PrimList x	1821	3	3
RepList x	1815	3	

LastFile c:\Rods\Data\Testing\fifoTesting\writeReadInpFifoA.pl

ID	PRIMITIVE	PrimLn	RepLn
1	POLL_REG_FIELD	9	5
2	RW_FIFO	1809	5
3	RW_FIFO	9	1805

Write and read back random data to Input FIFO A.

PrimitiveList Editor menu:

- **Main** - moves the *Main Panel* to the front.
- **Command List Editor** - opens *Command List Editor*.
- **Data File Creator** - opens *Data File Creator*.
- **Revision Update** – Converts old saved primitive lists (i.e. lists with outdated list or primitive revision number) to the new revision. Just select the directory with the lists for conversion (usually C:\RodTS) and all lists in this directory and all SUBDIRECTORIES will be automatically converted. After the conversion the status report file

PrimListRevisionUpdate_ERRORS.txt will appear. This text file lists only the files that couldn't be updated to the new revision and reason why. More detailed information about the last conversion is also available in *PrimListRevisionUpdate_STATUS.txt* (revision changes for each converted list). Both files are saved in *RodTS\Rcc\PrimListRevisionUpdate* directory.

Primitive List Editing

The right table on the PrimListEditor window is the edited *PrimitiveList*. The panel on the left shows the parameters for the selected primitive.

To add primitive to the list, select the desired primitive in the popup box first and then set the parameters for this primitive. A comment for this primitive can be entered into the bottom left text box (Enter = new line).

Then click (only **ONCE!!**) in the list window on the position (index) where you want to include the primitive. The position is highlighted by blue background. Add the primitive to the list using **ADD BEFORE, REPLACE, ADD AFTER** buttons (or use shortcut keys **Ctrl+ - ® -** instead).

To see the parameters for any primitive already included in the list, select the required primitive (one click on its' position in the list) and then press **EDIT** button (or **Ctrl+ ↵**), or better yet just simply **double click on the primitive** instead.

Another way to "look through" the list is using the keyboard up and down arrows (*View prim - ↑*). Set focus to the list table (click on the list) and then scroll up or down using *- ↑* keys, the parameter window on the left side is updated automatically for the currently specified primitive.

To include a slave list into the master list, select primitive **SEND_SLAVE_LIST**, then the slave number, add this primitive to the list and then click on the **EDIT SLAVE LIST** button. At this moment the right list table shows only the included slave list. When your slave list is done, press **DONE** button to return back to the main Master DSP list.

To delete primitive from the list, use **DELETE PRIM** button (**one** strong click or shortcut key *Ctrl+Delete*), to delete the list and clear the table **DELETE LIST**.

Before you save the list by **SAVE LIST** button, enter the list comment to the text box below the list (Enter = new line).

If you want to load already saved list, use **LOAD LIST** or **INSERT LIST** button. When using **LOAD LIST**, you are asked if the current list in the list table should be deleted. If you say yes, the current list will be deleted and replaced by the loaded list. If you say no, the loaded list will be included after the highlighted index in the current list (**INSERT LIST** does this directly).

The name of the last saved or loaded primitive list file is showed in the **LastFile** box above the list table. The saved lists are simple text files, which can be also viewed in any text editor.

However, editing outside the PrimListEditor is not recommended, except adding or changing the comments (enter the comment between the *- - - - -* and *=====* lines, number of comment lines is not strictly limited).

Primitive List example:

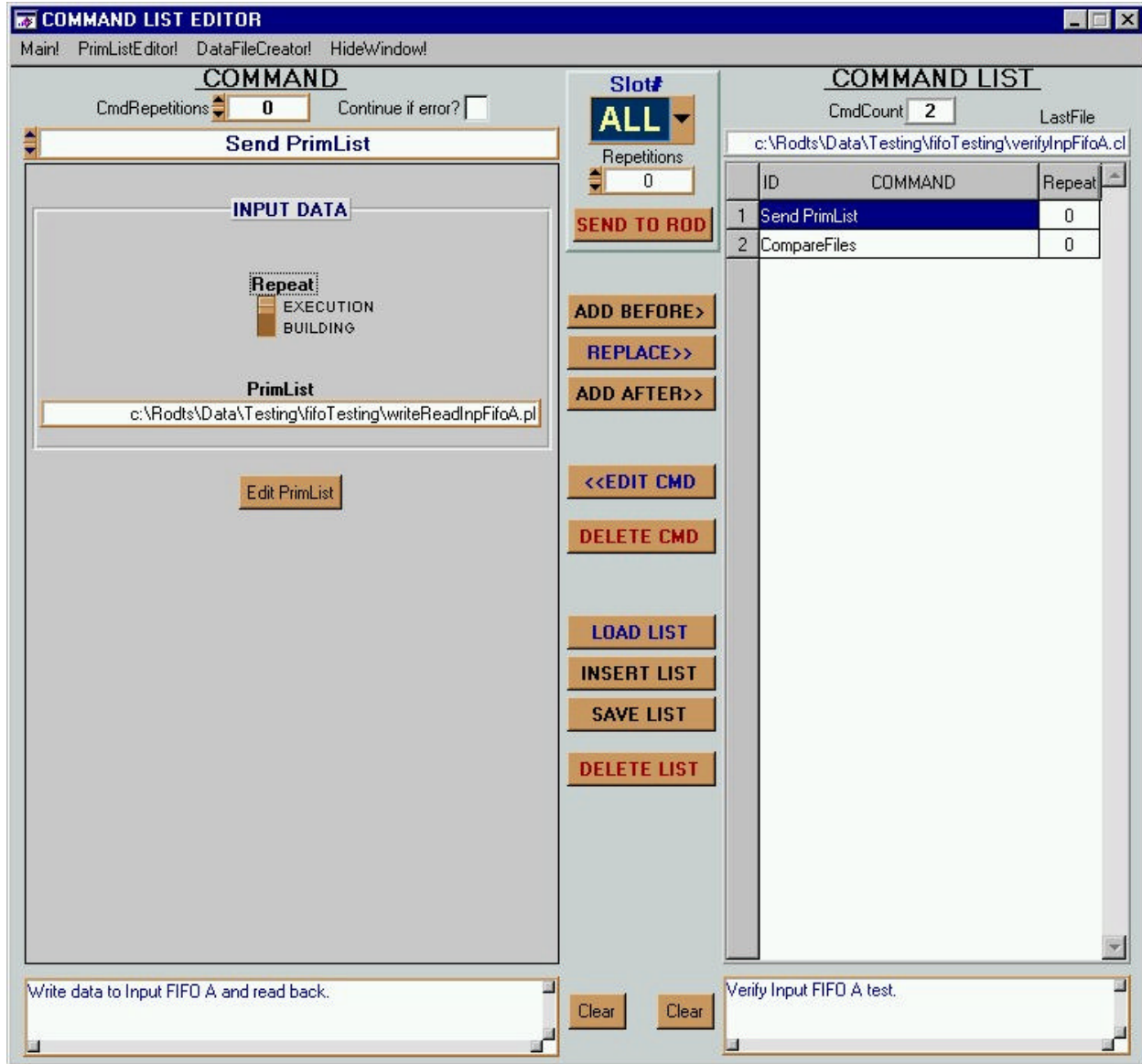
```
This is comment for the primitive list.  
The list contains one primitive (ECHO).  
-----  
length: 0x10A, numMsgs= 0x1, primListRevision= 111  
allPrimCount: 1, replyLength: 0x10A, replyCount: 0x1  
=====
```

This is comment for the first primitive (index 0).

```
-----  
index: 0, id: 0x0, primRevision: 100, length: 0x104, replyLength:  
0x104  
ECHO  
InputDataOption= 0  
testPattern= 0xABCDEF, testDataLength= 0x100
```

The PrimitiveList can be sent directly from the PrimListEditor table to any Rod for execution (the Rod must be loaded and initialized in TestStand, see chapter 3.).
Select the slot number, number of list repetitions (0 is executed once) and press **SEND TO ROD** button. The option Slave# ALL means the list will be sent to all loaded Rods.

6. Command List Editor



The user interface very similar to *PrimList Editor*. The table on the right is the CommandList, the panel on the left shows the parameters for the selected command.

Command List Editing

The function of all buttons is the same as in *PrimitiveListEditor*. No shortcut keys here, however. Above that you also have to enter the number of repetitions for each command in the command list (**CmdRepetitions** box: 0 means that the command will be executed once, 1 twice etc.) and select/deselect the option **Continue if error?**. Check this box if you want the command list execution to continue even if this command was not successful (this option can be overridden by the global *ContinueIfError* option, see chapters 2. and 3.).

Command List example:

```
This is comment for the command list.  
The list contains one command (SEND_PRIM_LIST).  
- - - - -  
commandCount: 1  
=====
```

This is comment for the first command (index 0).

```
- - - - -  
index: 0, id: 3, numRepetitions: 0, continueIfError: 0  
SEND_PRIM_LIST_COMMAND_ID  
RepeatExecNotBuild= 1  
FileName[]= c:\Rodts\Data\Testing\echo2.pl
```